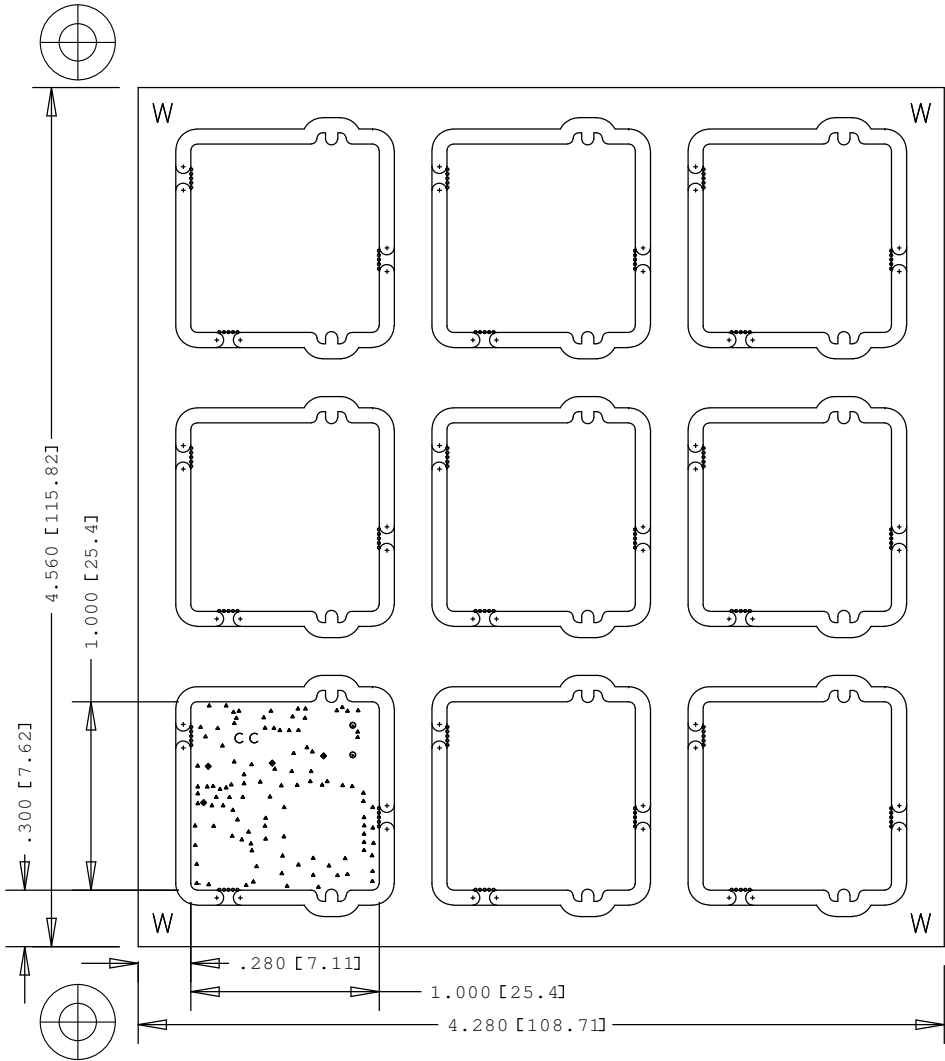


ElCAD File No. WILCOX_MEZZ.BRD		MANUFACTURING INFORMATION FOR PCB					
CUSTOMER: MARIS		P/N: M-P-00074-0001 REV-1.0					
LAYER STRUCTURE (from TOP to BOTTOM)							
LYR	DESCRIPTION	COPPER [Oz]	DIELECTRIC [Mil]	LAYER SYM	GERBER FILE	IMPEDANCE	
1.	COMPONENT SIDE	0.5+0.5		CS	xxx_CS.LGX	5.0/4.3/5.0 mil Diff. 90 Ohm	
2.	POWER PLANE	1.0		PP	xxx_PP.LGX		
3.	GROUND PLANE	1.0		GP	xxx_GP.LGX		
4.	PRINT SIDE	0.5+0.5		PS	xxx_PS.LGX		
NOTES: UNLESS OTHERWISE SPECIFIED: 1. TOTAL THICKNESS 1.2 mm +/- 10% AFTER PLATING 2. MATERIAL: FR4Tg=170 DEG.C MINIMUM 3. FINISHING: IMMERSION GOLD FINISH: IMMERSION GOLD 2-4 MICRO INCH THICKNESS, OVER ELECTROLESS NICKEL 150-170 MICRO INCH THICKNESS ON EXPOSED COPPER. 4. VIAS CONNECTED TO PLANE ARE DIRECT CONNECTION 5. HOLE DIAMETER IS AFTER PLATING 6. FINISHED HOLE SIZE TOLERANCE: +/-0.003" 7. VIA HOLE SIZE MAY BE MODIFIED BY MANUFACTURER 8. UNUSED VIA MAY REMOVED AS PRODUCTION NEEDS				9. PCB MFR SHOULD CLEAN SILKSCREEN BY SOLDER MASK 10. IN CASE OF CONFLICT BETWEEN MECHANICAL DIMENSIONS AND BOARD OUTLINE, GERBER FILE xxxxBO.LGX IS GOVERN 11. THE MANUFACTURER MAY WILL ADD TEAR DROPS IN ALL SIGNAL LAYERS IF NEED. 12. THE MANUFACTURER WILL ADD MANUFACTURER CODE, (FSCM) DATE AND UL-CODE IN P.S.ONLY. 13. ALL DIMENSIONS ARE IN INCH (mm) 14. FINISH BOARD SHALL MEET THE PERFORMANCE REQUIREMENTS OF IPC-6012 CLASS 2. 15. REMOVE ALL BURRS AND BREAK SHARP EDGES .015 INCH MAXIMUM.		16. BOARD BOW, WARP OR TWIST SHALL NOT EXCEED +/- 0.0075 PER INCH MEASURED IN ACCORDANCE WITH IPC-TM-650, METHOD 2.4.22. 17. CONTROLLED IMPEDANCE +/- 10% <input checked="" type="checkbox"/> YES <input type="checkbox"/> NO 18. PERFORM VCUT AS PER VIEW:A <input type="checkbox"/> YES <input checked="" type="checkbox"/> NO 19. DO NESTING (BREAKAWAY) AS PER DRAWING <input checked="" type="checkbox"/> YES <input type="checkbox"/> NO 20. RoHS COMPLIANCE: 20.1 RoHS COMPLIANT MARK/STAMP ON EACH PCB FRAME 20.2 RoHS COMPLIANT ON EACH PCB SEALED BAG/PACKAGE 20.3 IN ALL PACKAGING LEVELS AND SHIPPING DOCUMENTS IT SHOULD BE MARKED WITH A VISIBLE RoHS COMPLIANT MARK OR LABEL 20.4 C.O.C.SHOULD INCLUDE A DECLARATION THAT THE PCB'S ARE RoHS COMPLIANT	
ELECTRICAL TEST					GERBER FILES NAME CONVENTION: <u>XXXXXXYY.LGX</u> <div><div>LAYER CODE</div><div>BOARD NAME</div></div>		
ELECTRICAL TEST FILE: XXXXX.IPC (IPC356D FORMAT) IF FILE XXXXX.IPC IS MISSING PLEASE DO NOT PRODUCE THIS BOARD AND CALL ELCAD							
Artwork by ELCAD PCB Ltd Tel: Office: 972-8-941-7274, 972-8-941-7284 CP: 972-522-560-866, e-mail: elcad@zahav.net.il							



DRILL CHART: TOP to BOTTOM				
ALL UNITS ARE IN MILS				
FIGURE	SIZE	TOLERANCE	PLATED	QTY
.	10.0	+2.0/-4.0	PLATED	105
.	12.0	+2.0/-4.0	PLATED	4
c	28.0	+3.0/-3.0	PLATED	2
.	16.0	+4.0/-0.0	NON-PLATED	135
.	24.0	+2.0/-2.0	NON-PLATED	2
W	98.0	+4.0/-4.0	NON-PLATED	4

TOTAL HOLES: 252